

The Processor

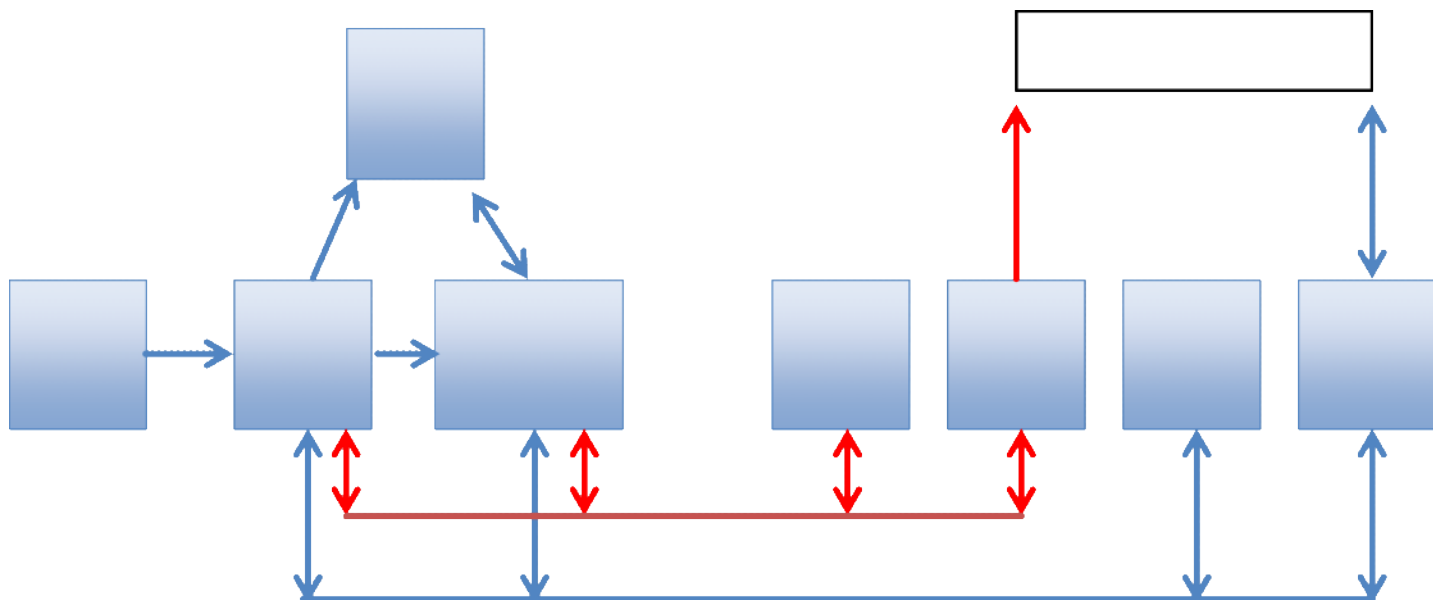
13 March 2019 08:07

Typical Processor

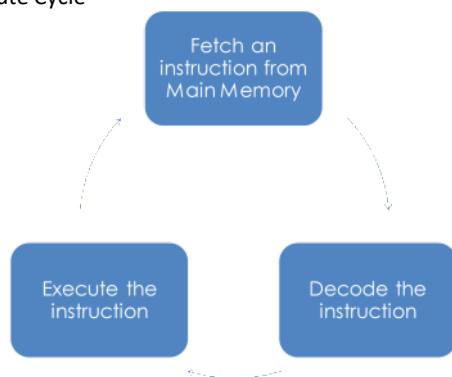
Name	
Image	
Description	

Purpose of the CPU

Processor Design



Fetch Execute Cycle



Fetch	
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Decode	
Execute	

Buses

Bus	Purpose

Registers

Register	Purpose
Program Counter (PC)	
Memory Address Register (MAR)	
Memory Buffer Register (MBR) (AKA MDR)	
Current Instruction register (CIR)	